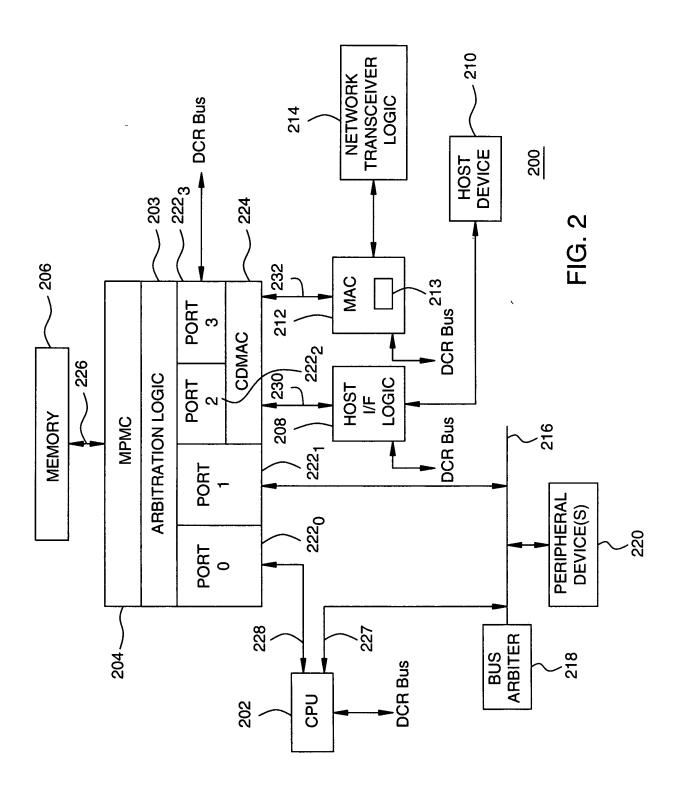


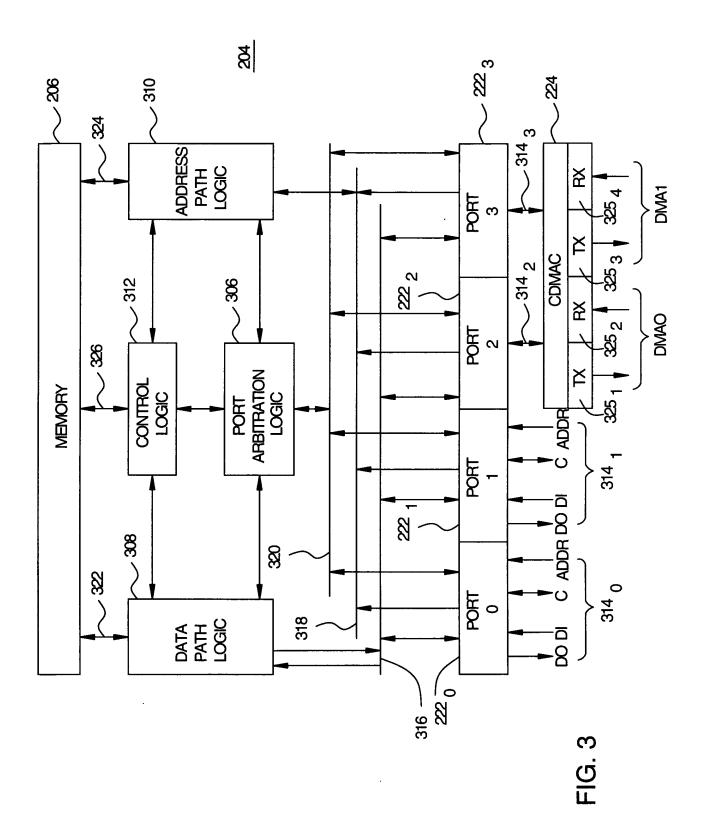
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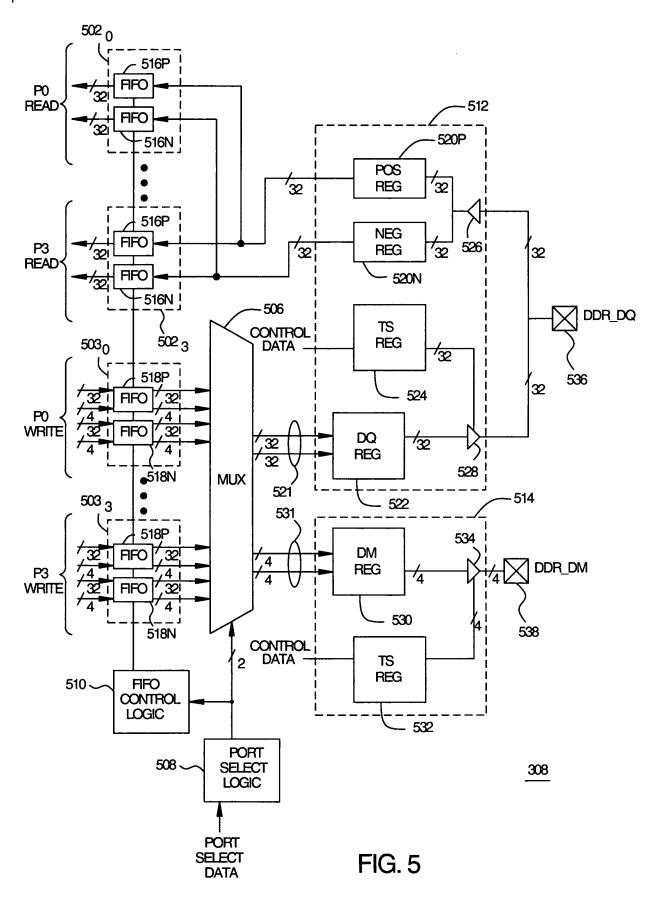
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			r 	, , , , ,
98	TS4B	РЗ	Ā	P0
13 TS4	TS4A	L.	P0	P1
	TS3B	P2	P1	P0
TS3	TS3A	Ф	P0	P1
	TS2	P1	P0	
	TS1	PO	P1	
		PR1	PR2	PR3





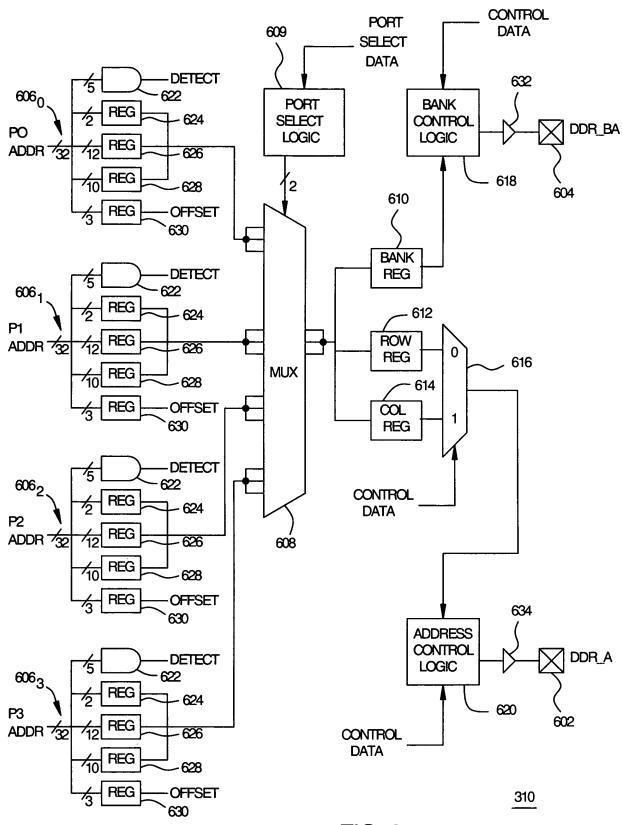
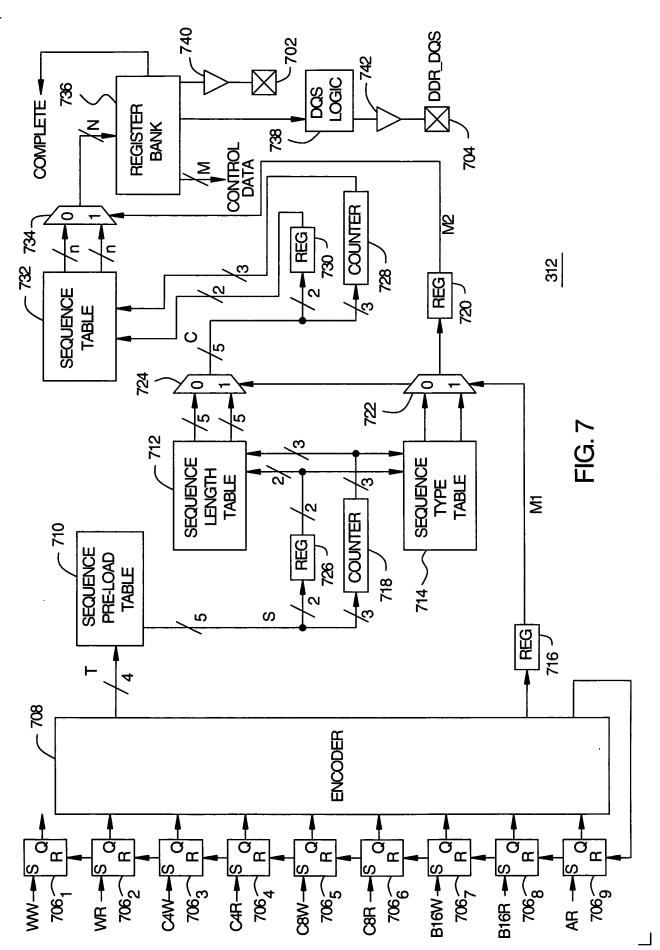
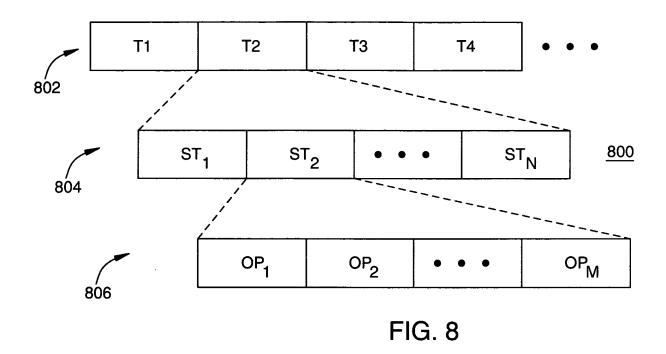


FIG. 6

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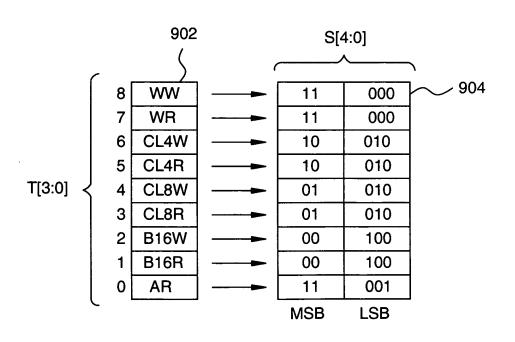


FIG. 9

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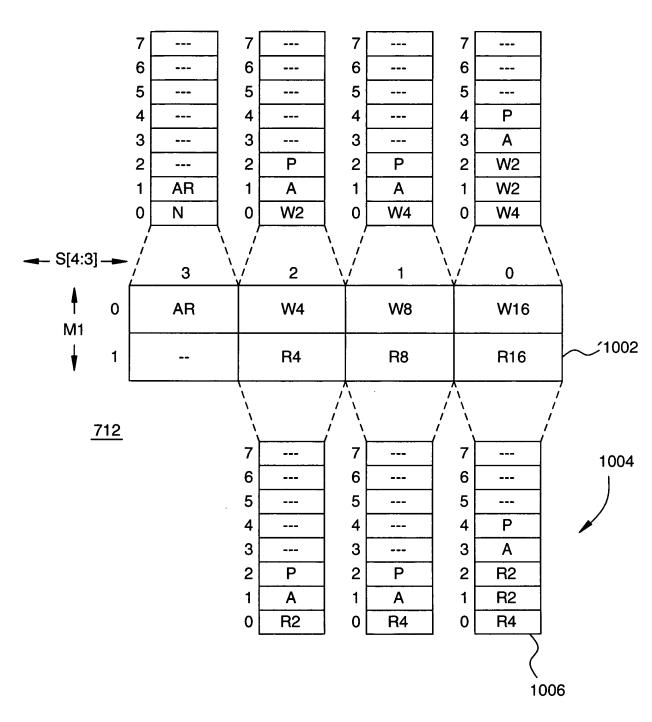


FIG. 10

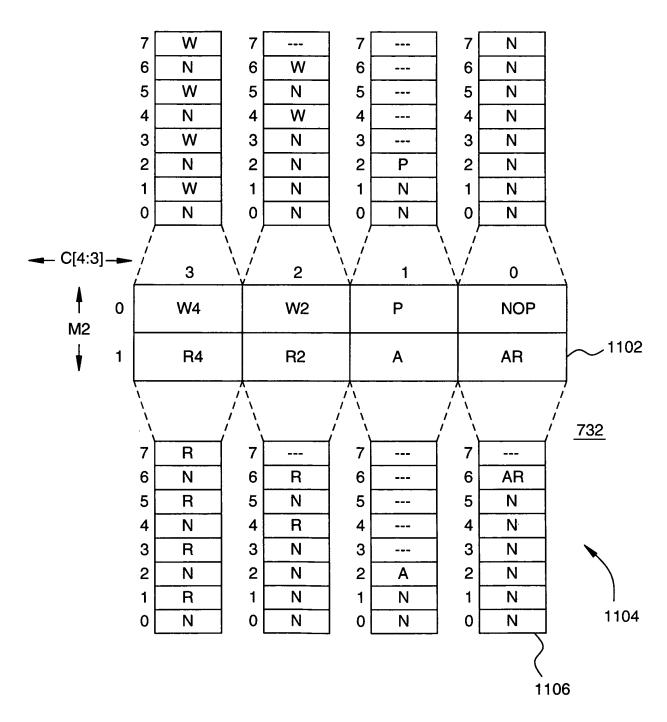
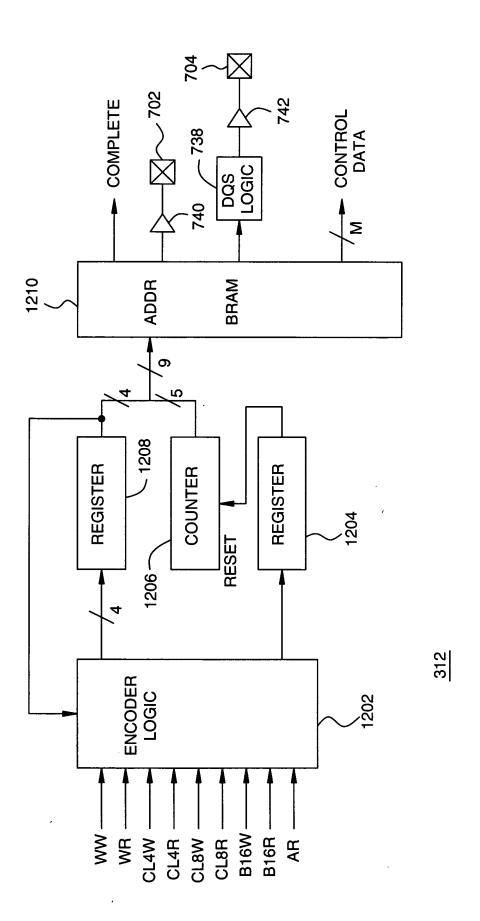


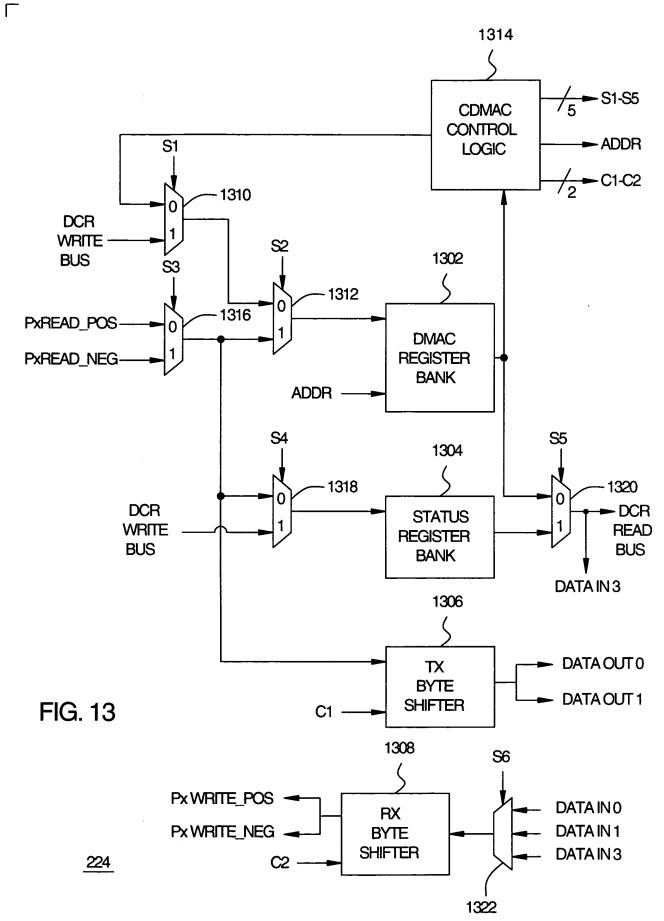
FIG. 11

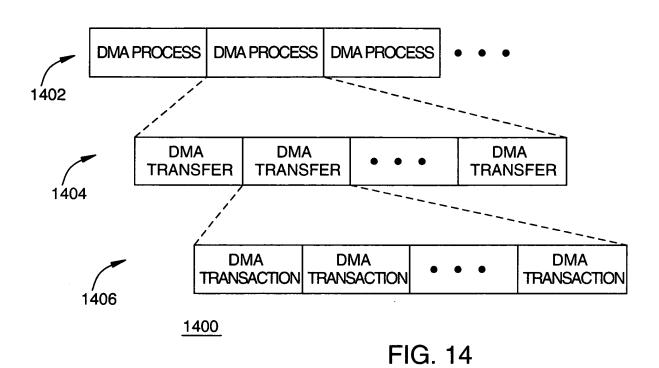


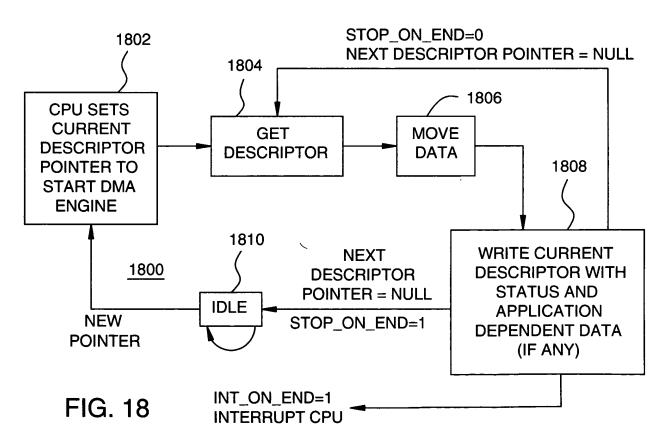
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FIG. 12

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1508 NEXT DESCRIPTOR POINTER CDMAC TX1 ENGINE 1510 **CURRENT ADDRESS** 1512 **CURRENT LENGTH 1514** CURRENT DESCRIPTOR POINTER **NEXT DESCRIPTOR POINTER** CDMAC RX1 ENGINE **CURRENT ADDRESS** 1502 **CURRENT LENGTH** CURRENT DESCRIPTOR POINTER **NEXT DESCRIPTOR POINTER** CDMAC TX2 ENGINE **CURRENT ADDRESS** 15023 **CURRENT LENGTH CURRENT DESCRIPTOR POINTER NEXT DESCRIPTOR POINTER** CDMAC RX2 ENGINE **CURRENT ADDRESS** 15024 1500 **CURRENT LENGTH** CURRENT DESCRIPTOR POINTER 1504 **CDMAC TX1 STATUS** 15042 **CDMAC RX1 STATUS CDMAC TX2 STATUS CDMAC RX2 STATUS** FIG. 15 **INTERRUPT** 1506

	HEADER	DATA SECTION	FOOTER
FIG. 17	1702	1704	1706
		<u>1700</u>	



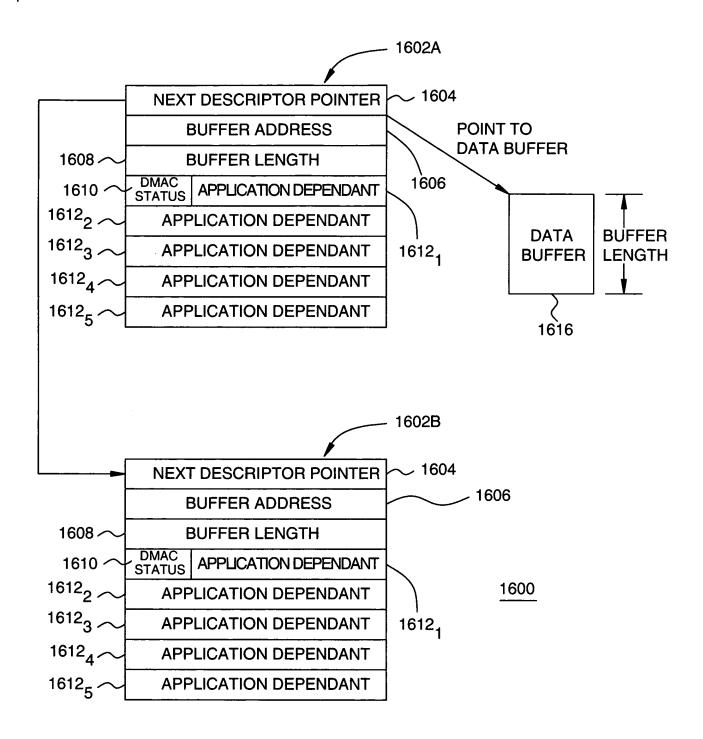


FIG. 16

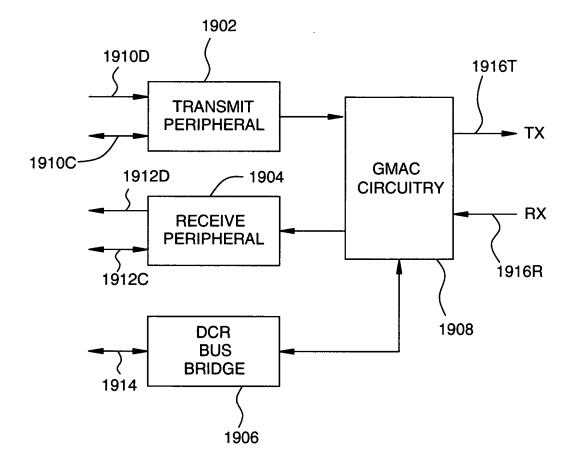
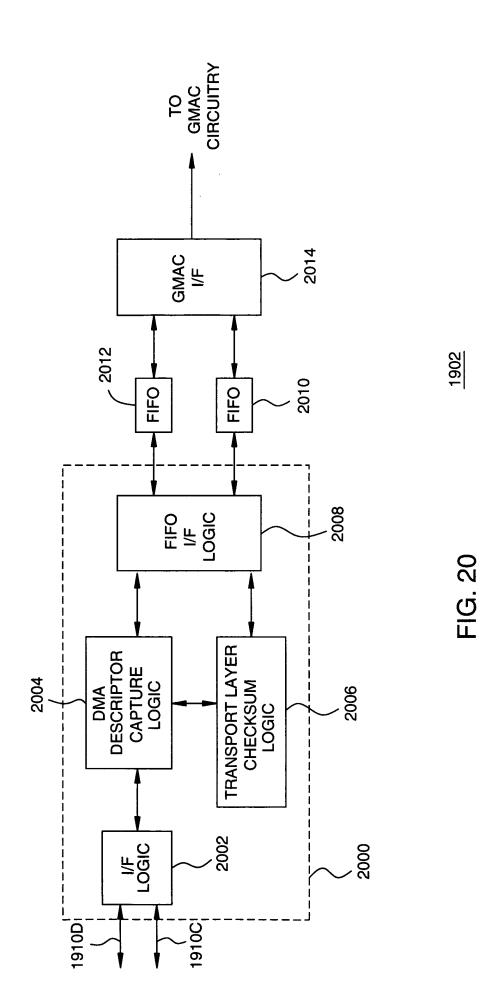


FIG. 19



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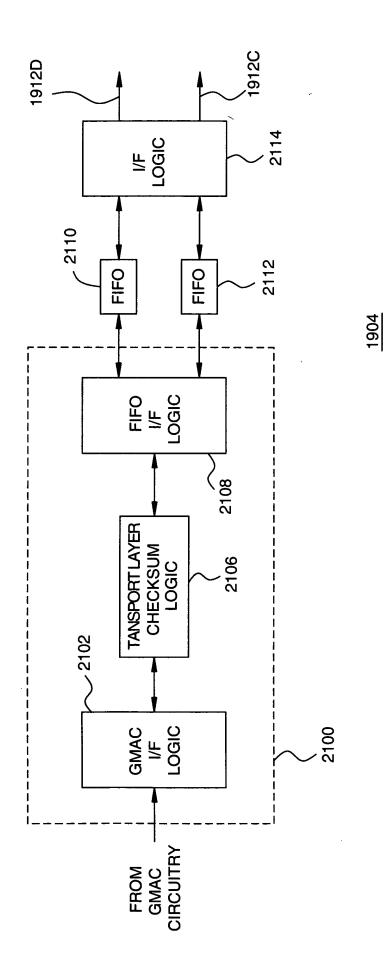


FIG. 21